

S1C88 Manual errata

ITEM Oscillation Circuits and Operating Mode		
Object manuals	Document code	Object pages
S1C88349 Technical Manual	404522102	44
S1C88832/862 Technical Manual	MF1215-02	30
S1C88816 Technical Manual	404520905	28
(Addition)		
Note: When CPU is operating in OSC3, after canceling the HALT status, you should allow a minimum wait time of 400 usec to execute the HALT instruction again for VD1 stabilization.		