

S1C60N08 Manual errata

ITEM		
Object manuals	Document code	Object pages
S1C60N08/60R08 Technical Manual	MF1282-02	I-34
<p>(error: sentences with under line)</p> <p>4.7.1 Configuration of serial interface</p> <p>The S1C60N08 Series has a built-in synchronous clock type 8 bits serial interface that can be enabled by mask option. The configuration of the serial interface is shown in Figure 4.7.1.1.</p> <p>The CPU, via the 8 bits shift register, can read the serial input data from the SIN (P10) terminal. Moreover, via the same 8 bits shift register, it can convert parallel data to serial data and output it to the SOUT (P11) terminal. The synchronous clock for serial data input/output may be set by selecting by software any one of 3 types of master mode (internal clock mode: when the S1C60N08 Series is to be the master for serial input/output) and a type of slave mode (external clock mode: when the S1C60N08 Series is to be the slave for serial input/output).</p> <p><u>Also, when the serial interface is used at slave mode, SIOF (R11) signal which indicates whether or not the serial interface is available to transmit or receive can be output to output port R11 by mask option.</u></p> <p>(correct: sentences with under line)</p> <p>... S1C60N08 Series is to be the slave for serial input/output).</p> <p><u>Also, SIOF (R11) signal which indicates whether or not the serial interface is available to transmit or receive can be output to output port R11 by mask option at both master and slave modes. And serial interface status bit SIOF (2ECH-D1) is available by the same mask option.</u></p>		

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S1C60N08/60R08 Technical Manual	MF1282-02	I-36
<p>(error: sentences with under line) 4.7.3 Data input/output and interrupt function (4) SIOF signal <u>When the serial interface is used in the slave mode (external clock mode), SIOF is used to indicate whether the internal serial interface is available to transmit or receive data for the master side (external) serial device. SIOF signal is generated from output port R11 by mask option.</u> SIOF signal becomes "1" (high) when the S1C60N08 serial interface becomes available to transmit or receive data; normally, it is at "0" (low). SIOF signal changes from "0" to "1" immediately after "1" is written to SCTRG and returns from "1" to "0" when eight synchronous clock has been counted.</p> <p>(correct: sentences with under line) <u>SIOF signal is generated from output port R11 by mask option at both the master and slave modes.</u> <u>For example, when the serial interface is used in the slave mode (external clock mode), SIOF can be used to indicate whether the internal serial interface is available to transmit or receive data for the master side (external) serial device. In the master mode, the status SIOF (2ECH-D1) can be read as the serial interface status.</u> SIOF signal <u>and the value of SIOF (2ECH-D1)</u> become "1" (high) when the S1C60N08 serial interface becomes available to transmit or receive data; normally, it is at "0" (low). SIOF signal <u>and the value of SIOF (2ECH-D1)</u> change from "0" to "1" immediately after "1" is written to SCTRG and returns from "1" to "0" when eight synchronous clock has been counted.</p>		

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S1C60N08/60R08 Technical Manual	MF1282-02	I-37
<p>(error) 4.7.4 Mask option (7) Output port R11 may be assigned as SIOF (R11) output terminal which will indicate whether the serial interface is available to transmit or receive signals.</p> <p>(correct: words with under line are added) 4.7.4 Mask option (7) Output port R11 <u>and R11 output port data bit (2ECH-D1)</u> may be assigned as SIOF (R11) output terminal <u>and serial interface status SIOF (2ECH-D1)</u> respectively which will indicate whether the serial interface is available to transmit or receive signals. <u>If this mask option is selected, R11 output terminal and R11 data bit (2ECH-D1) cannot be used as general purpose output terminal and data register.</u></p>		

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S1C60N08/60R08 Technical Manual	MF1282-02	I-40
<p>(error) 4.7.5 Control of serial interface SIOF (R11): Serial interface status (2ECH-D1) Indicates the running status of the serial interface. When "1" is read out : RUN status When "0" is read out : STOP status Writing : Invalid The RUN status is indicated from <u>immediaterly</u> after "1" is written to SCTRG bit through to the end of serial data input/output.</p> <p>(correct: words with under line are added) ...The RUN status is indicated from <u>immediately</u> after "1" is written to SCTRG bit through to the end of serial data input/output. <u>This readable only bit can be used when SIOF (R11) output terminal is selected by mask option.</u></p>		

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S1C60N08/60R08 Technical Manual	MF1282-02	I-24
<p>(error) 4.4.4 Mask option (2) When "Use" is selected, a maximum delay of 1msec occurs from the time interrupt condition is established until the interrupt factor (IK) is set to "1".</p> <p>(correct) (2) When "Use" is selected, the interrupt circuit contains $f_{osc1}/8(4kHz)$ clock noise reject circuit, the input must be held at interrupt condition for at least $16/f_{osc1}(0.5msec)$ to assure an input interrupt. In this case a maximum delay of 0.5msec occurs from the time interrupt condition is established until the interrupt factor (IK) is set to "1".</p>		