

■ EPD Controller

A controller IC dedicated to the electronic paper product from E Ink Corporation.

This is an epoch-making product equipped with an architecture that maximizes the characteristics of electronic paper, allowing for pen input, text input, and other features that have thus far been difficult to achieve.

Product	CPU Interface Support	Built-in Memory	External Memory	Maximum Resolution	Grayscale	Supply Voltage		Additional Features	Package
						Core	IO		
S1D13521B01B	16-bit I/F Indirect addressing	-	SDRAM /F (for frame buffer)	4096 x 4096	2/3/4/5bpp	1.65V to 1.95V	1.65 to 1.95 2.70 to 3.60	16 area partial update	PFBGA8-181 PFBGA12-180
S1D13522A00B	16-bit I/F Indirect addressing or Serial I/F	2MB	-	1024 x 768	2/3/4bpp	1.65V to 1.95V	1.65 to 3.60	15 area partial update, Picture in Picture with Transparency	PFBGA10-144

■ S1D13521 System Block Diagram

